

MAR 15 2005

PATENT

Docket No.: JCLA8534-R

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Applicant : JIN-YUAN LEE et al.
Filed : HERewith
For : INTEGRATED CHIP PACKAGE STRUCTURE USING
ORGANIC SUBSTRATE AND METHOD OF
MANUFACTURING THE SAME

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing TWO (2) U.S. reference(s) that first page of which is enclosed. This Information Disclosure Statement is being filed with this application, and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1).

Dated: 3/15/2005

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Respectfully submitted,

By: 
Jiawei Huang
Registration No. 43,330

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.: JCLA8534-R	APPLICATION No.: 10/055,499
	APPLICANT: JIN-YUAN LEE et al.	
	FILING DATE: January 22, 2002	GROUP: 2829

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	2002-0074641	06/20/02	Steven Towle et al.	257	692	
	2002-0070443	06/13/02	Xiao-Chun Mu et al.	257	712	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER	DATE CONSIDERED
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EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.